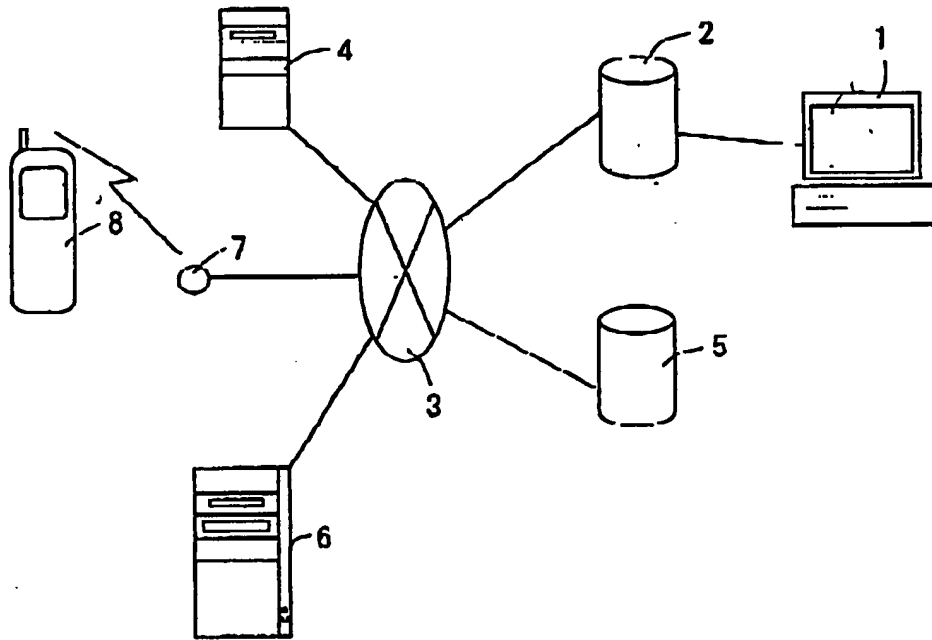


1 / 2 1

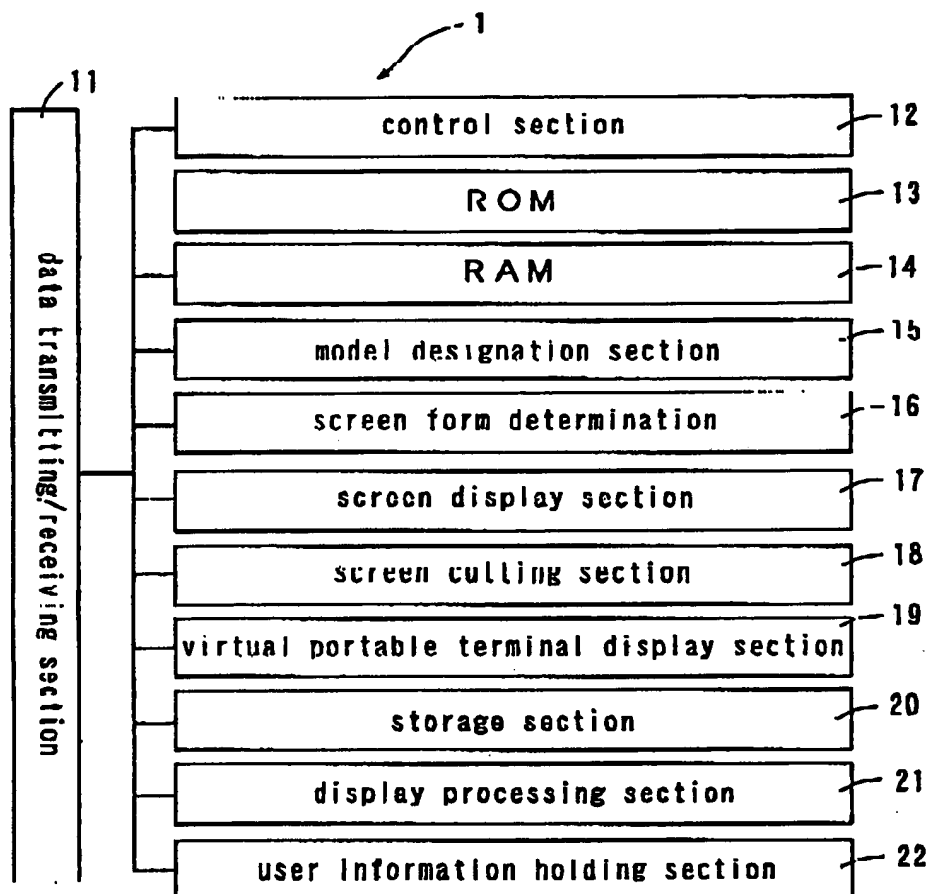
Fig. 1
图 1



0985393.052501

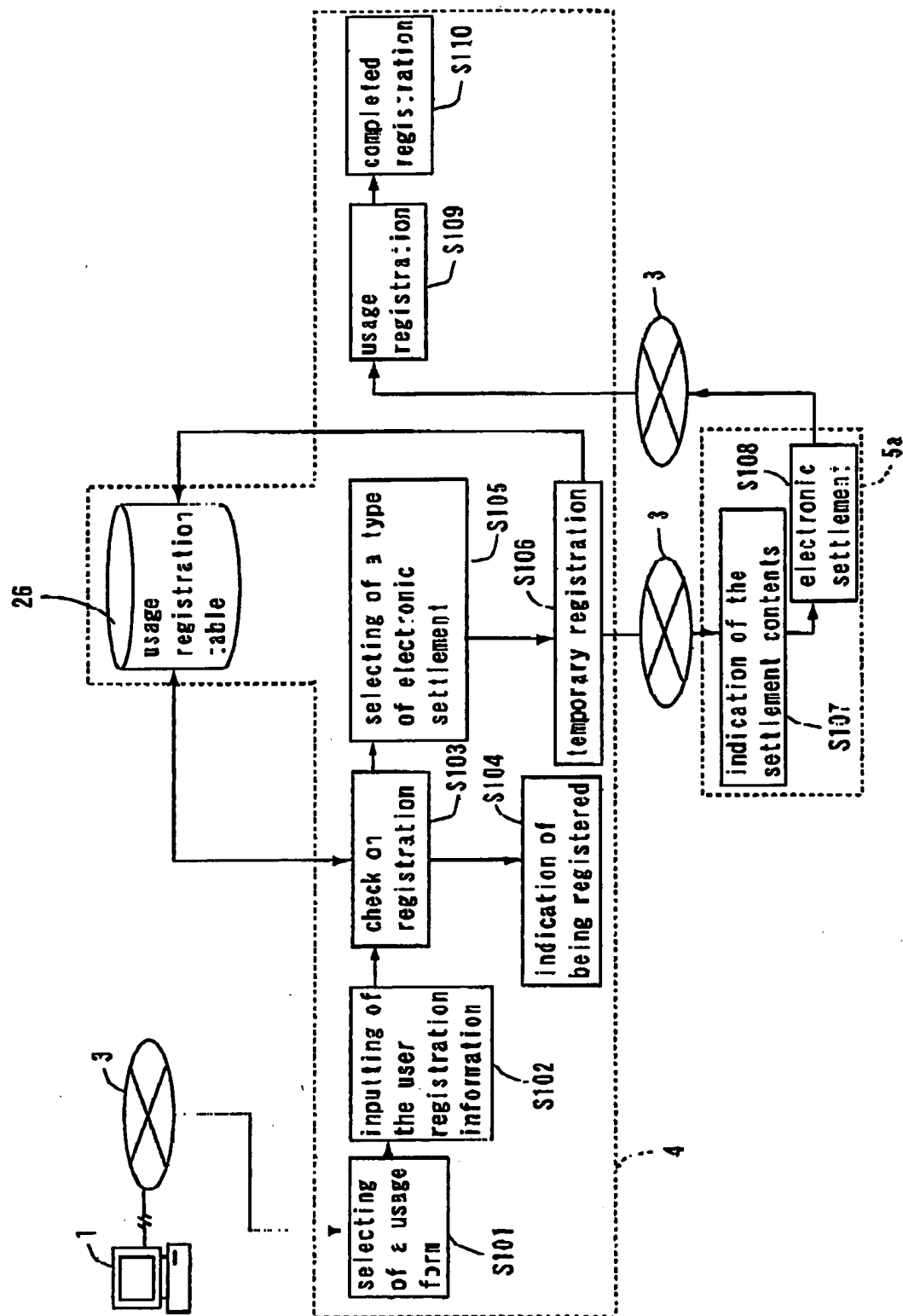
A100076-2

Fig. 2



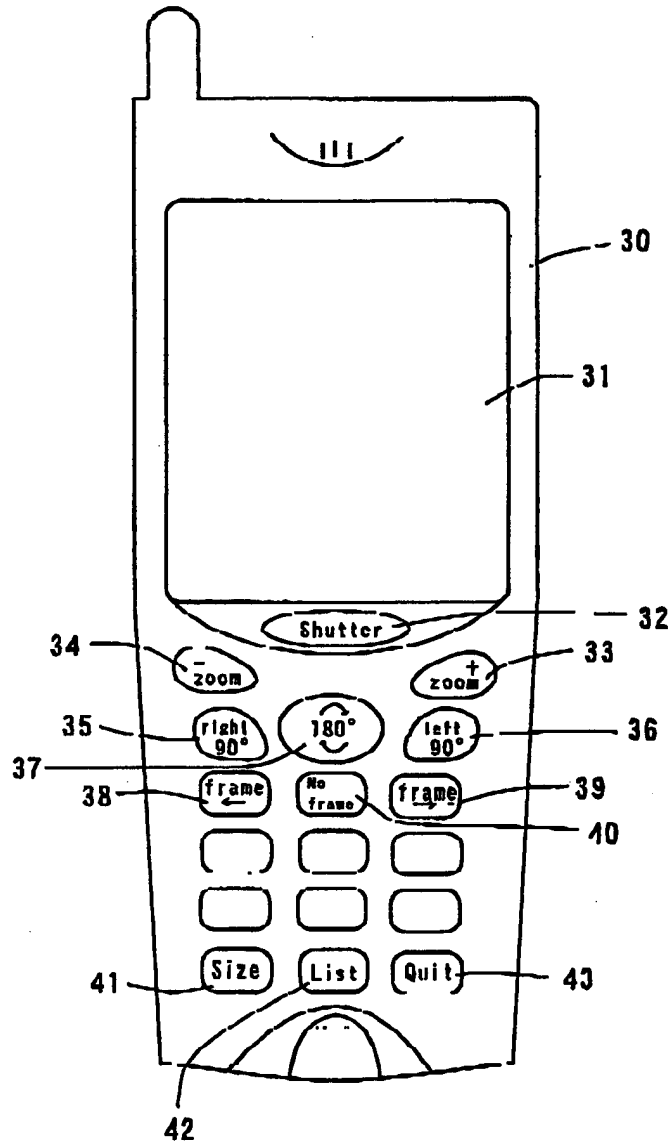
096529-05501

Fig. 3



A100076-4

F:q.4

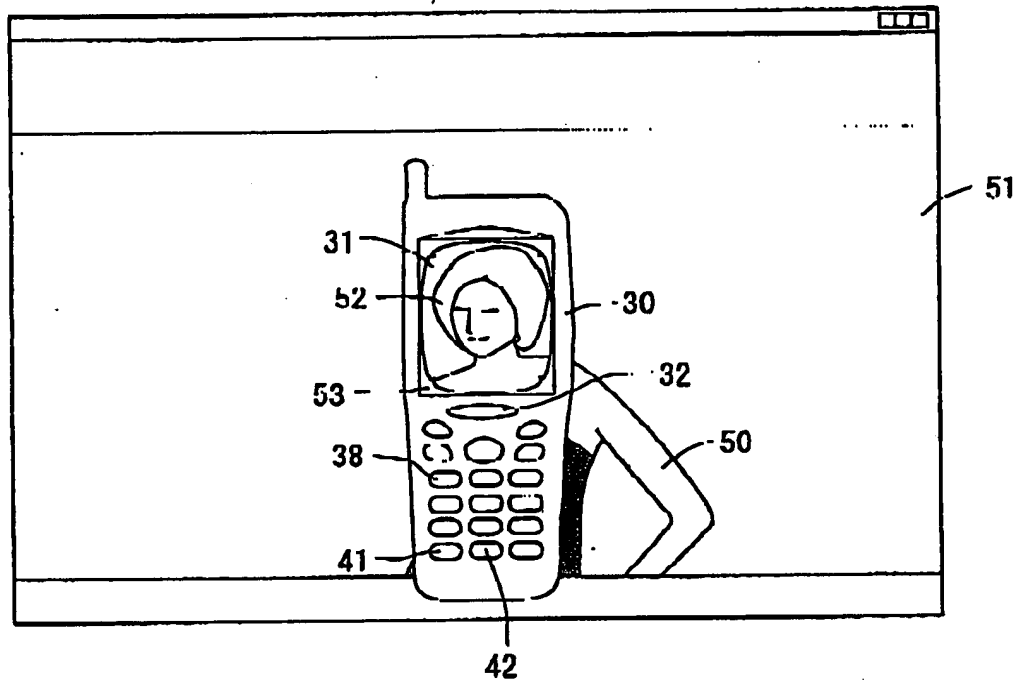


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Fig. 5

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~~Fig. 5~~

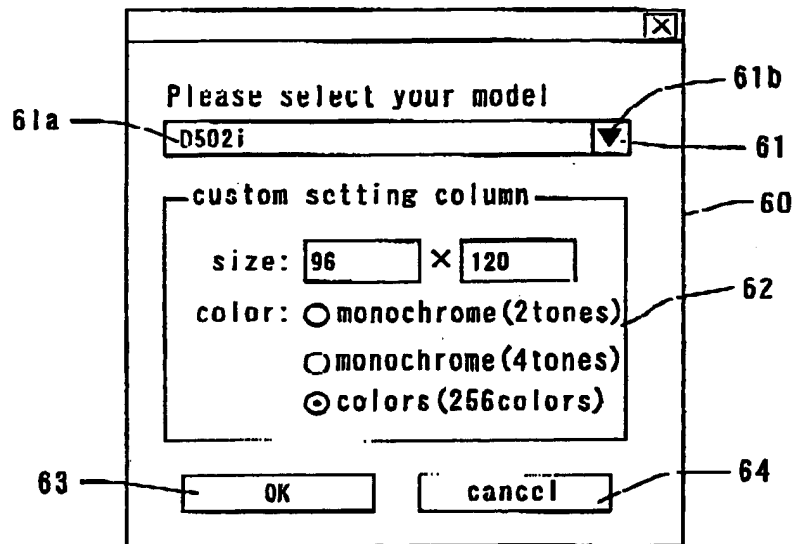


09065293-052501

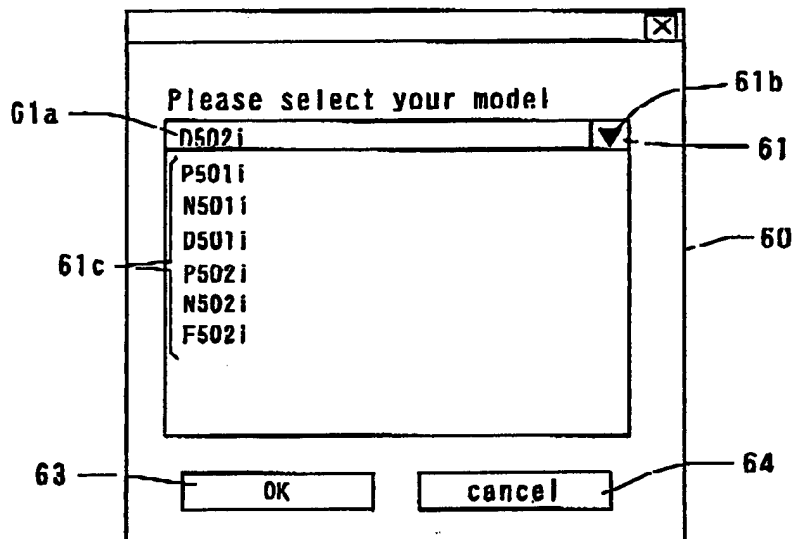
A100076-G

Fig. 6

(A)



(B)



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Fig. 7

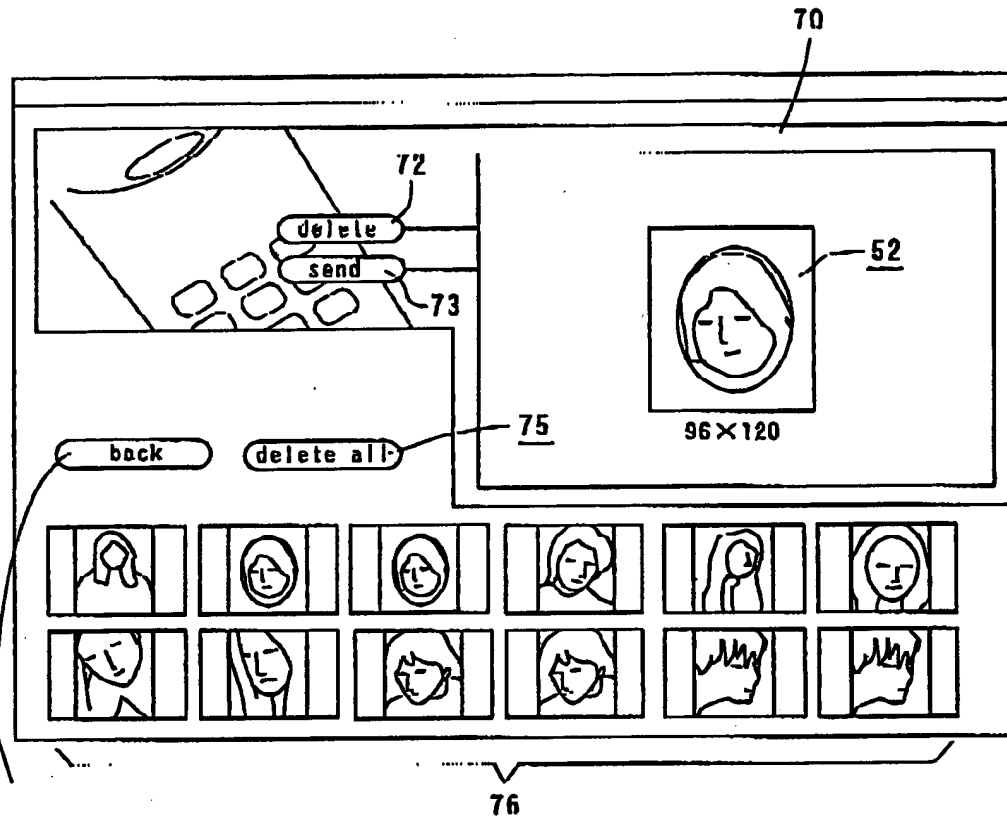
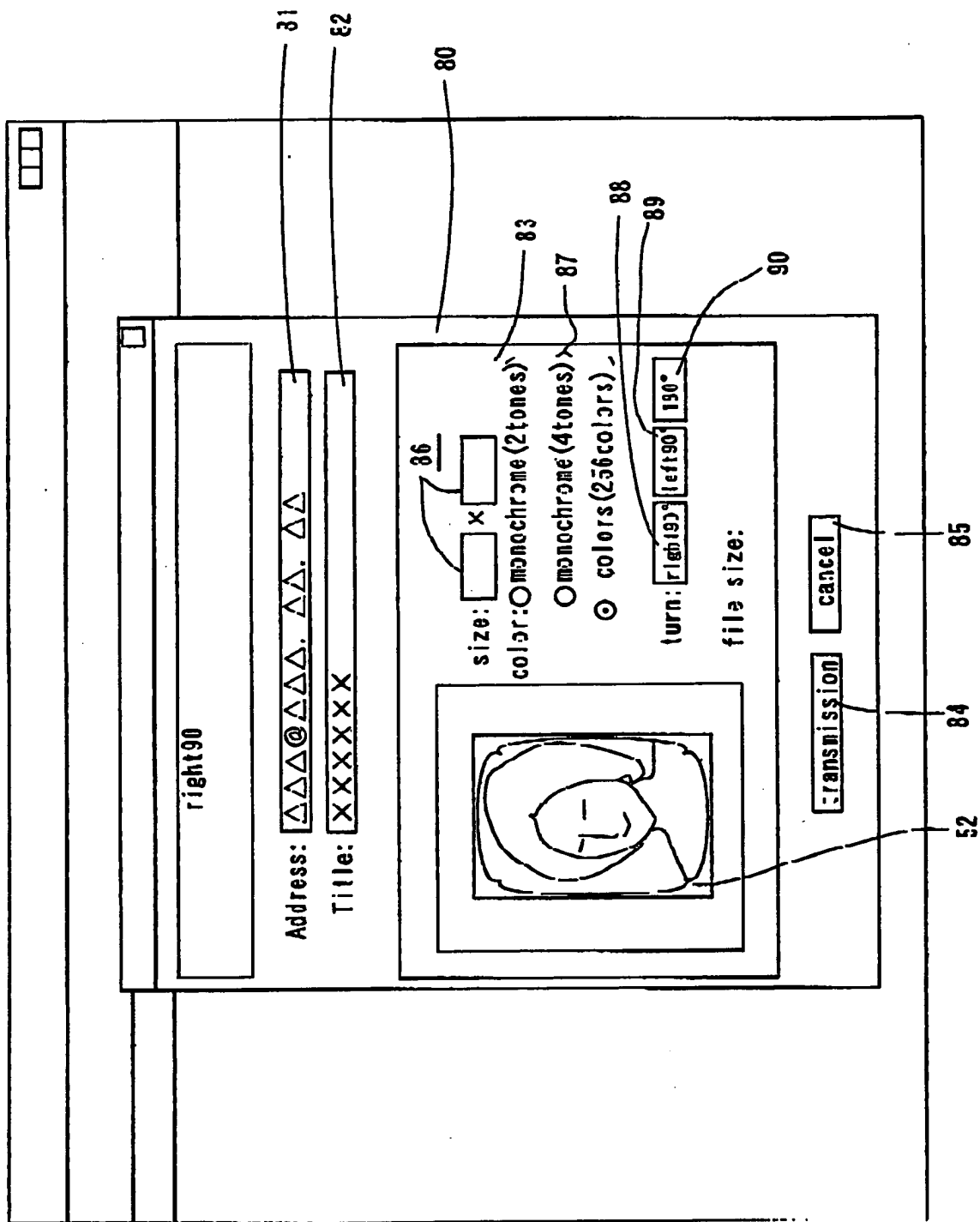


FIG. 7

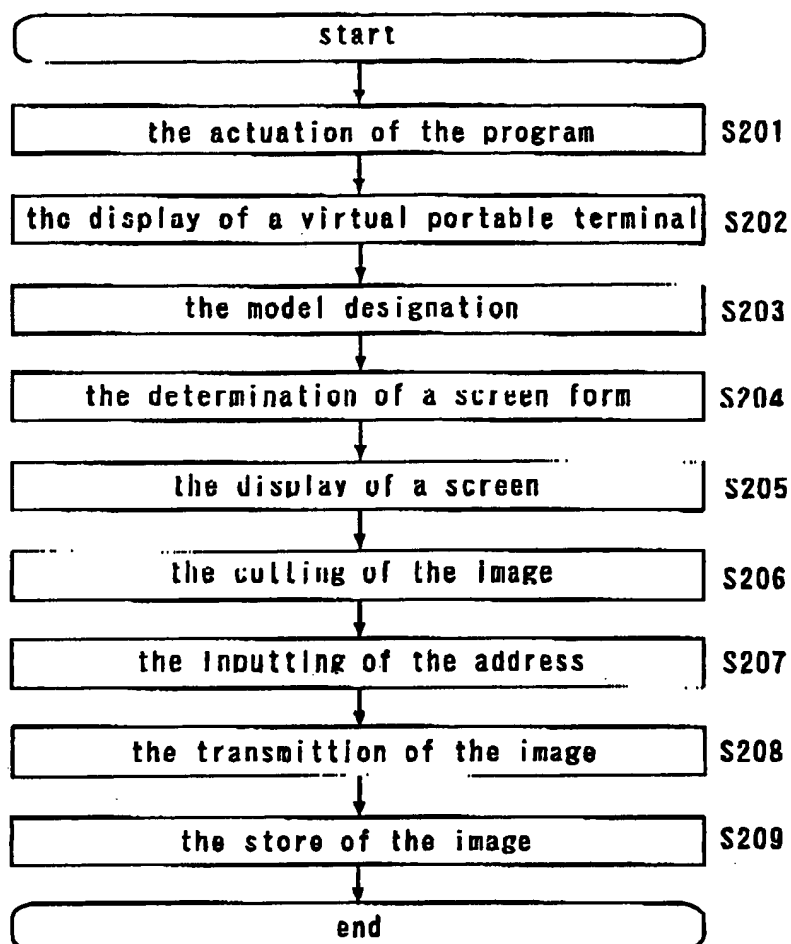
Fig. 8

FIG. 8



A100076-9

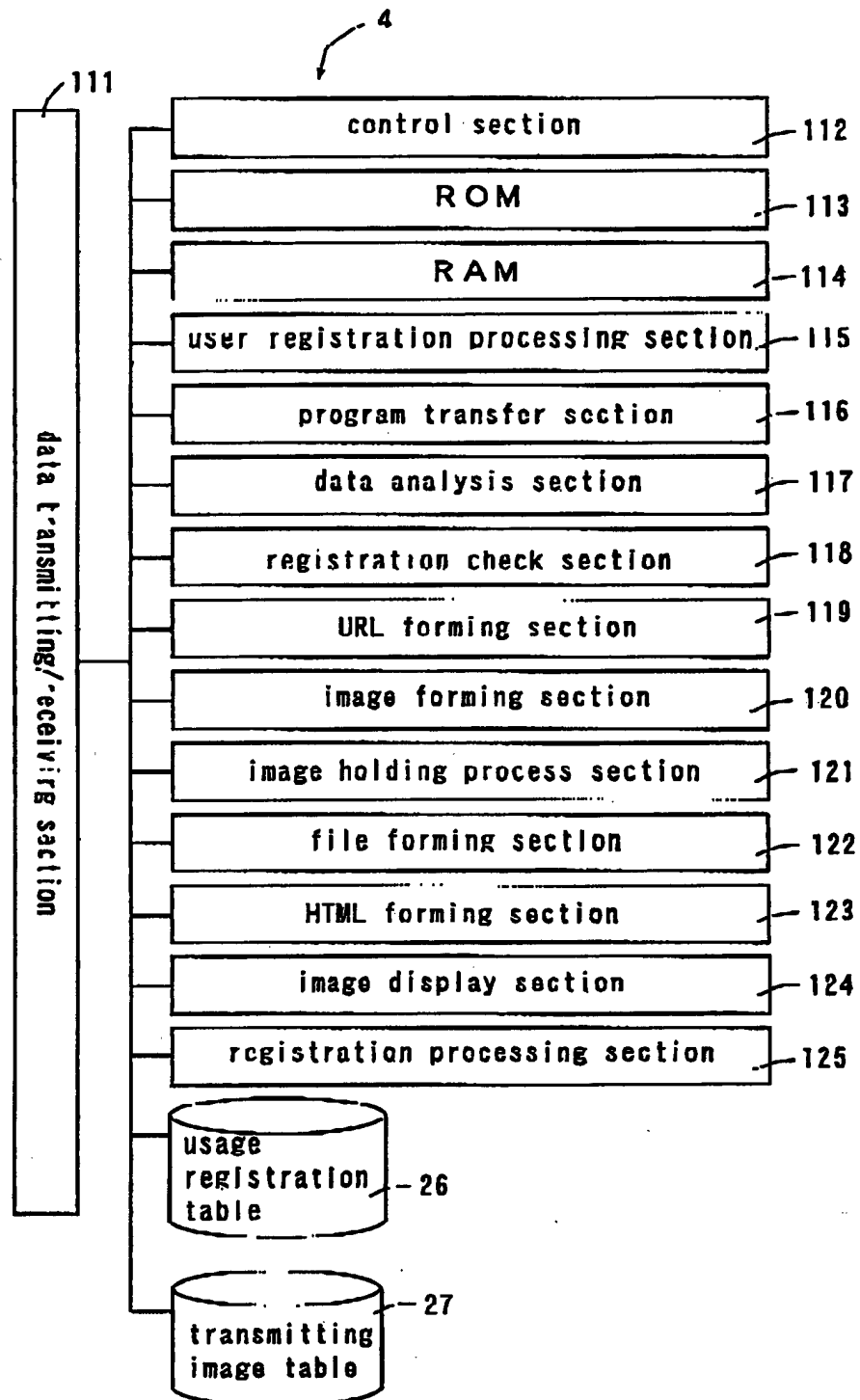
Fig. 9



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A00368-10

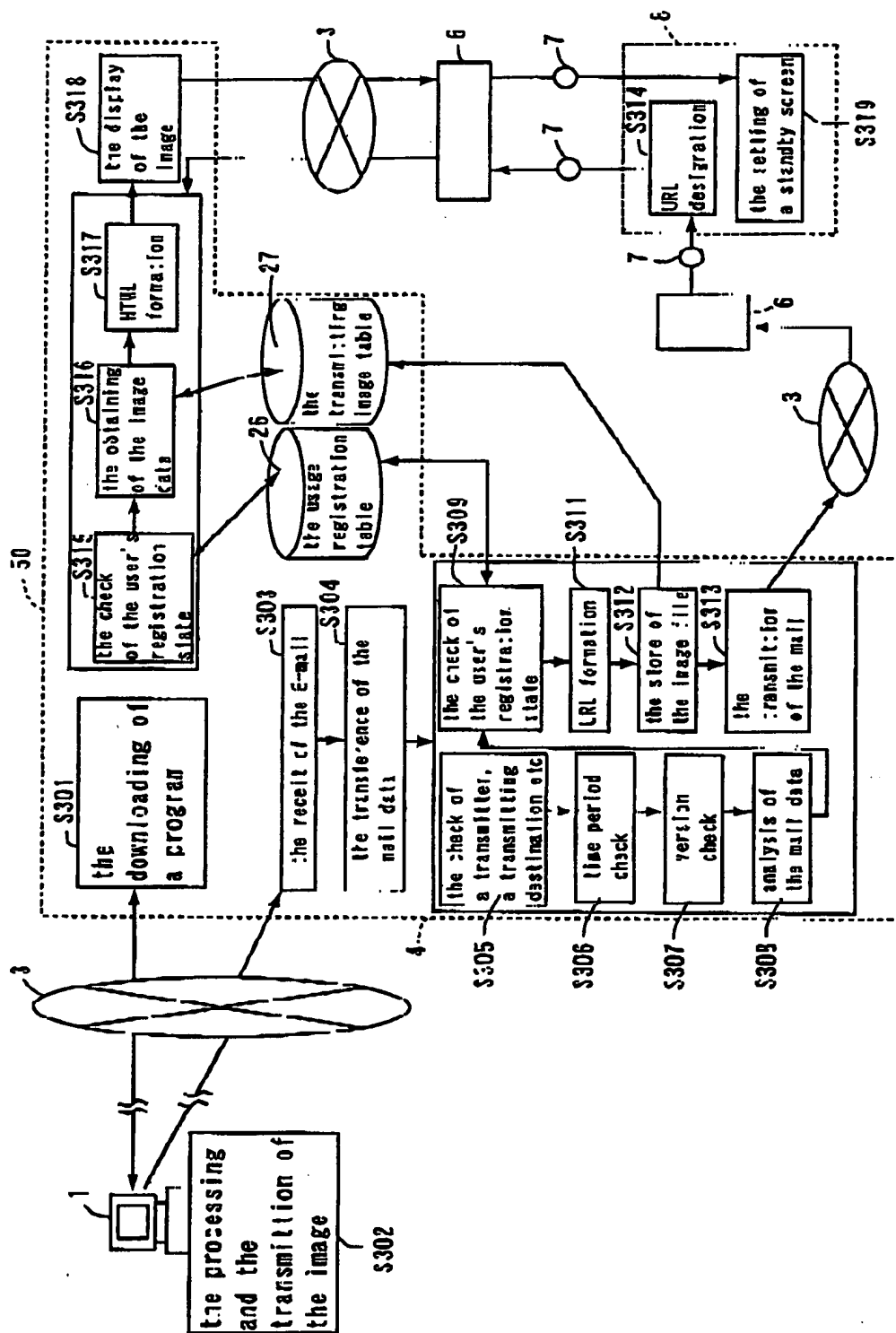
Fig. 10



T0350-2659360

A100076-11

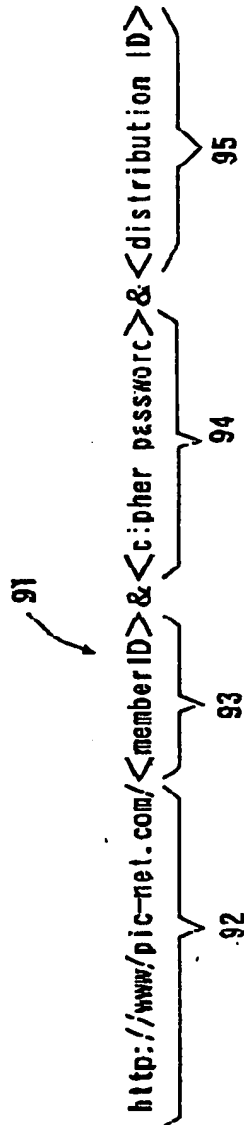
Fig. 11

[illegible]

A100076-12

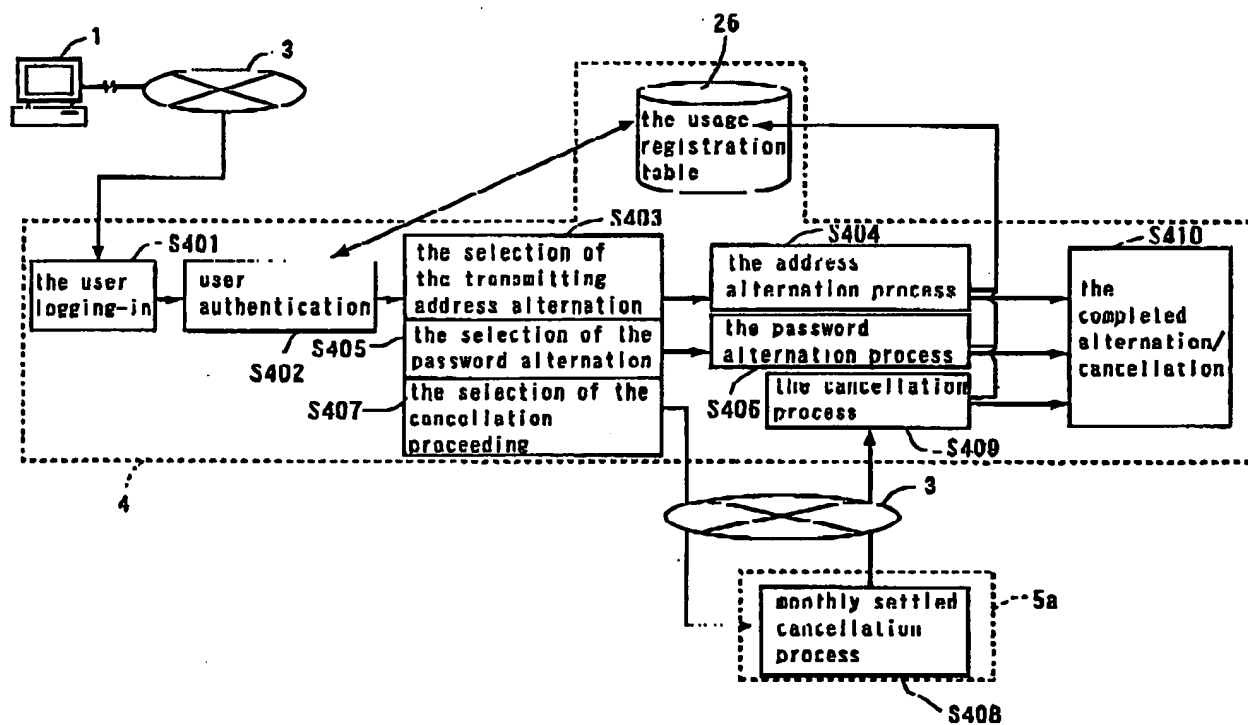
Fig. 12-

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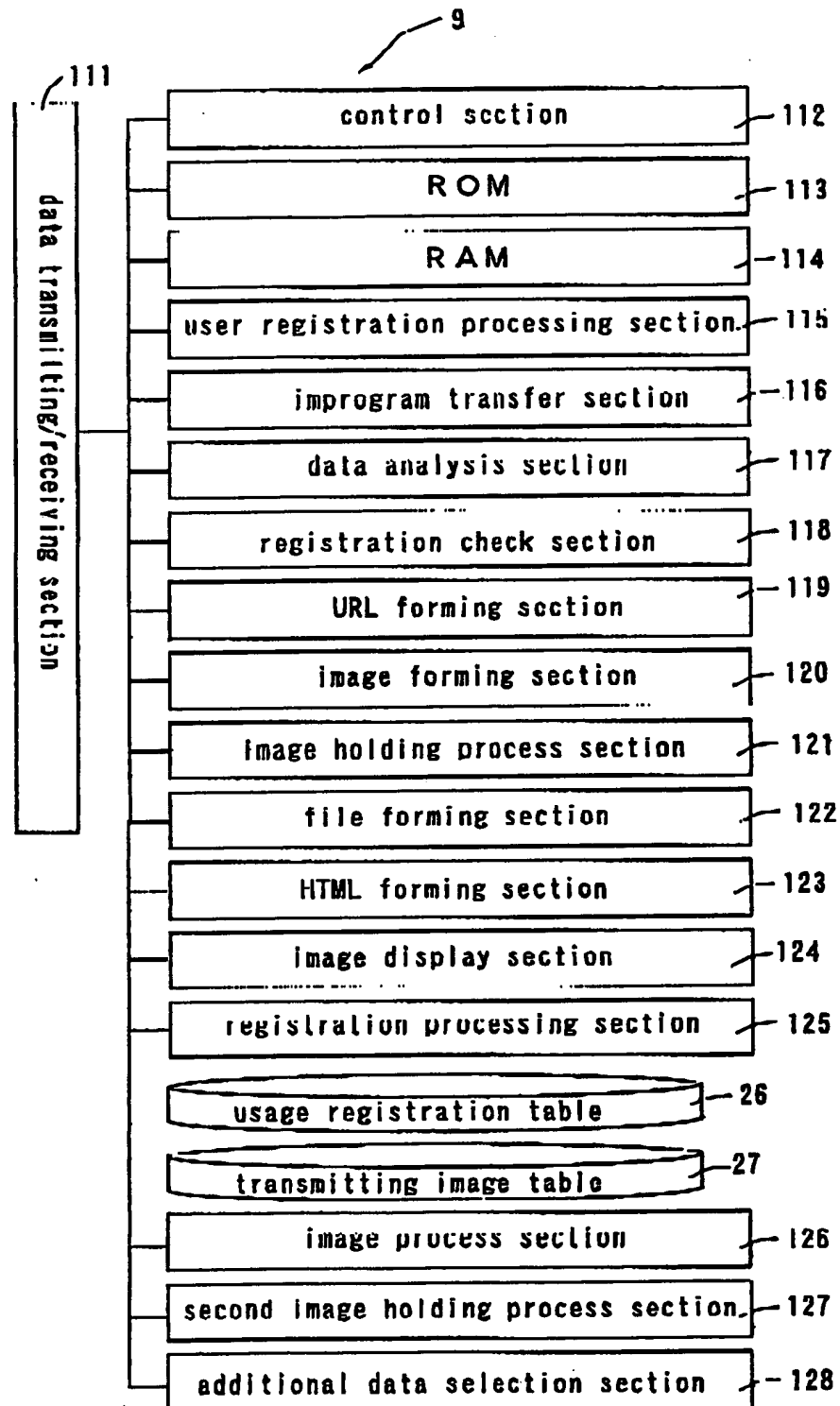
A100076-13

Fig. 13



A100076-15

Fig. 15



data transmitting/receiving section

control section

ROM

RAM

user registration processing section. — 115

improgram transfer section

data analysis section

registration check section

URL forming section

image forming section

image holding process section

file forming section

HTML forming section

image display section

registration processing section

usage registration table

transmitting image table

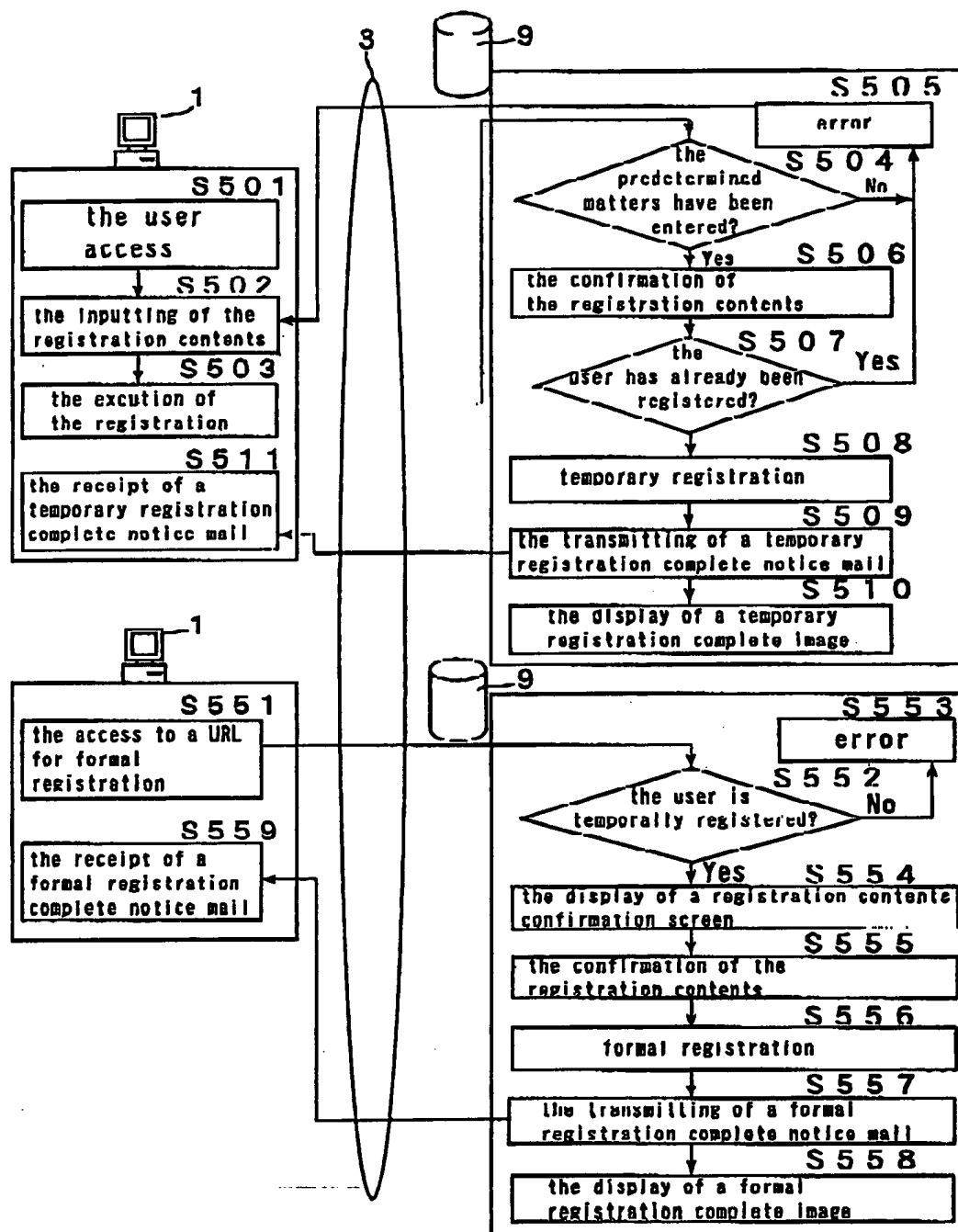
image process section

second image holding process section. — 127

additional data selection section

[16]

Fig. 16



T05250-26259860

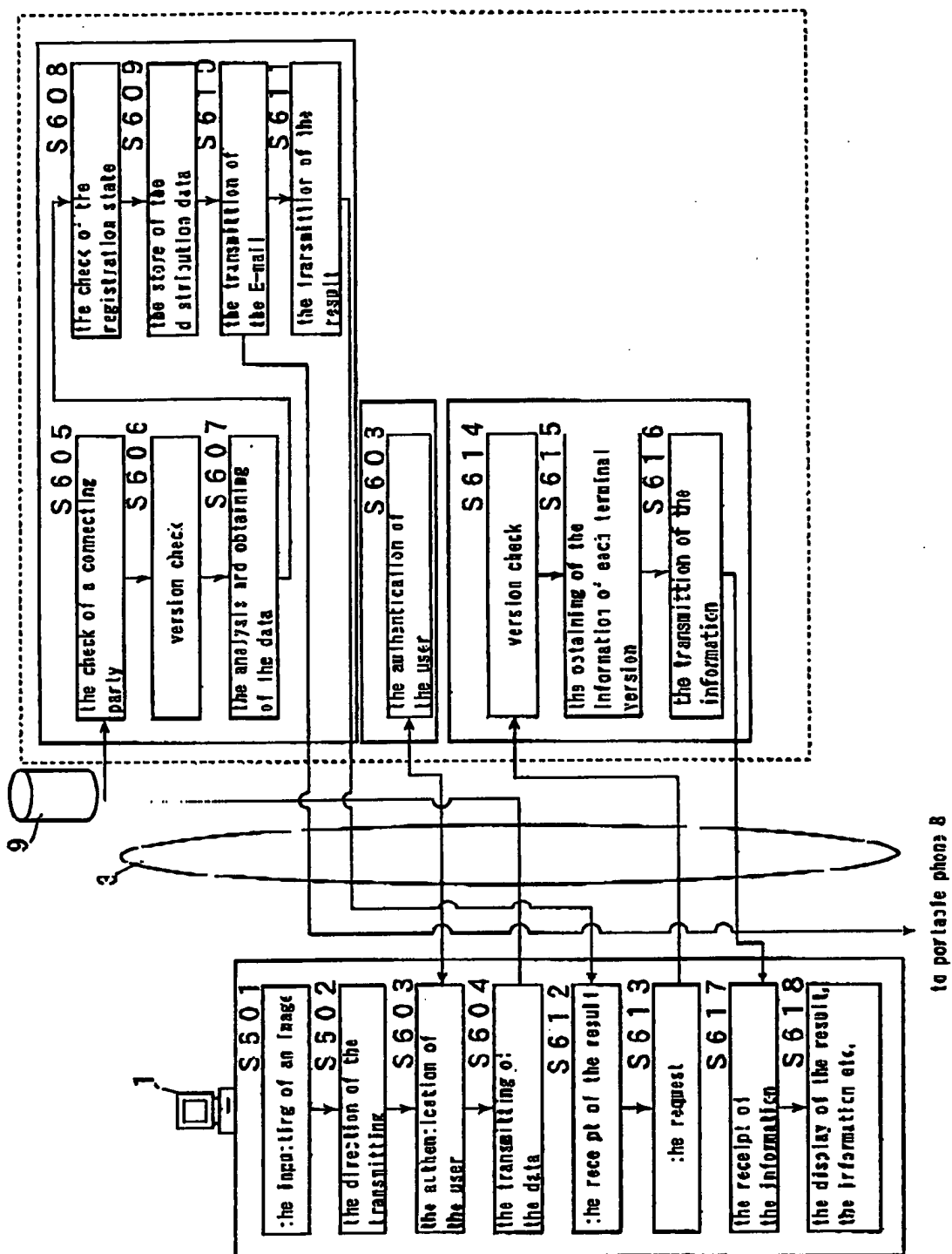
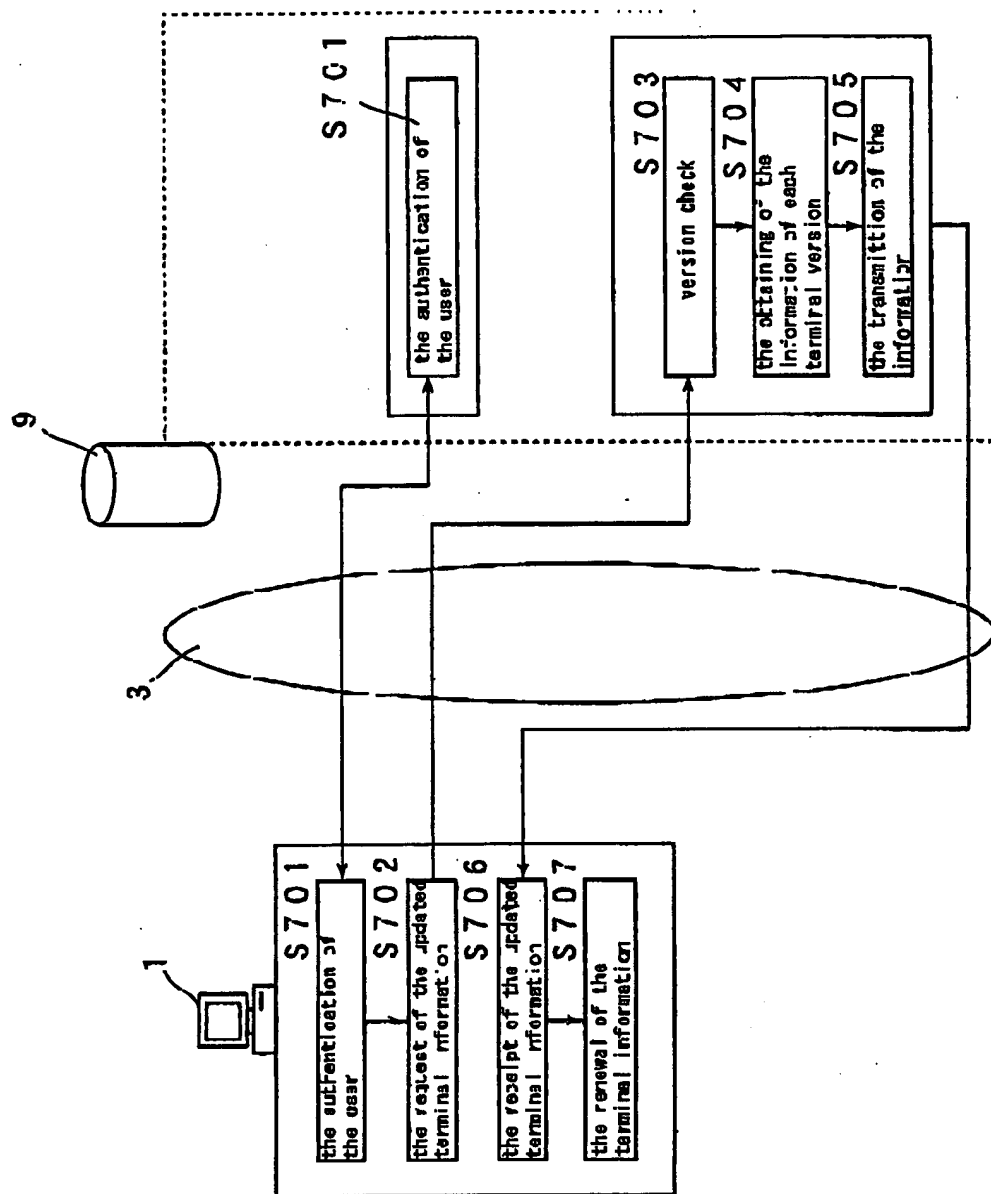
[illegible]

FIG. 19

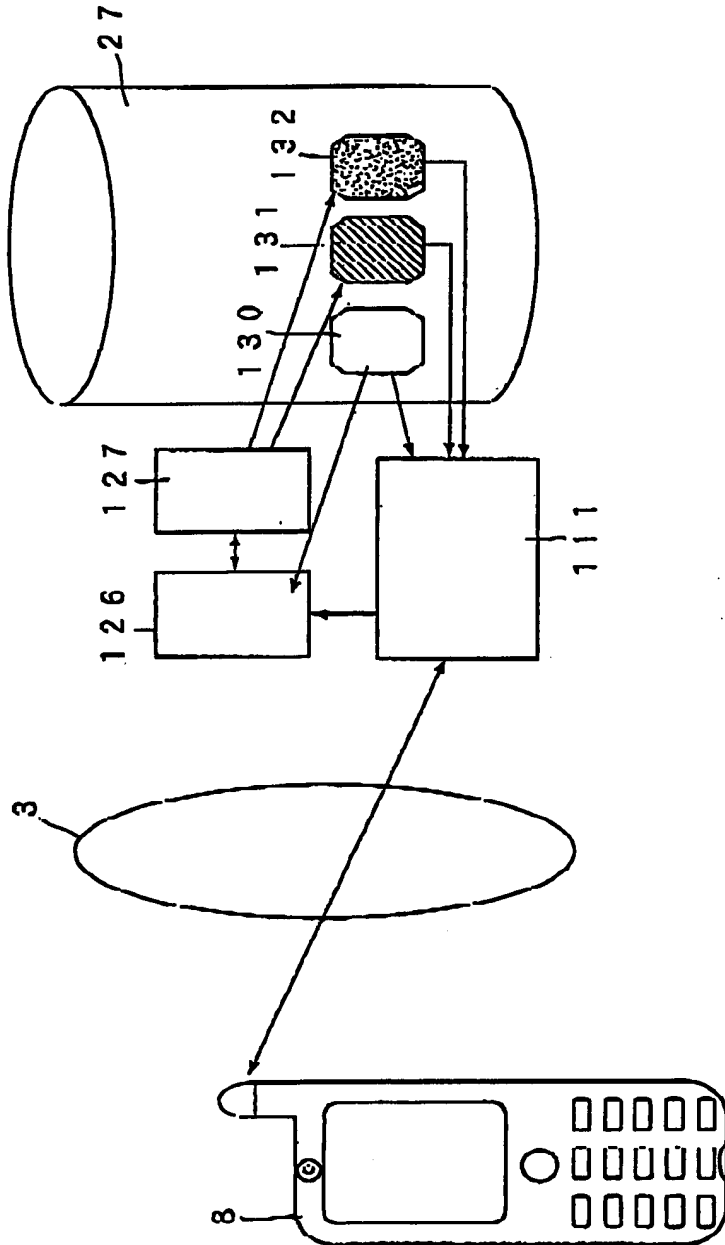
Fig. 19

FIG. 19



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图20 Fig. 20



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Fig. 21
2-1

